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STAT	EMENT BY APP	LICANT	First Named Inventor	Min-Hyung Cho	
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Examiner Cite		Document Number	Publication Date		Pages Coherens Lines Whom	
1-14-1-0	Cite No.1	Number - Kind Code ² (if known)	or leave Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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	No.¹	Country Code ³ - Nurriber ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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			Application Number			
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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	
KT		"A VLSI Architecture for a High-Speed All-Digital Quadrature Modulator and Demodulator fo rDigital Radio Applications, H. Samueli, et al., IEEE Journal, vol., 8, No. 8, Oct. 1990, pges 1512-1519		
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